

The multicore era has started. Are 40 years of sequential source code obsolete?

3rd FITTEST Industrial Day, 7th IEEE International Conference on Research Challenges in Information Science (RCIS 2013, Sorbonne, Paris)

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```
129 // Überprüft, ob es sich bei den angegebenen .NET-Assemblies
130 // 32-Bit-Anwendung handelt.
131 // -----
132 private bool Is32BitAssembly(string filename)
133 {
134     bool is32Bit = true;
135     string tmpFilename = workingDir + @"corflags.txt";
136     // Konsolenausgabe wird zur späteren Verarbeitung in Text-Datei
137     string corflags = CreateCmd(false, "corflags \"\" + filename +
138                                     "\" + tmpFilename +
139     Process proc = new Process();
140     proc.StartInfo.FileName = "\"\" + corflags + "\"";
141     proc.StartInfo.UseShellExecute = false;
142     proc.StartInfo.WorkingDirectory = workingDir;
143     proc.StartInfo.CreateNoWindow = false;
144     try
145     {
146         proc.Start();
147         proc.WaitForExit();
148     }
149     catch { }
150 }
```



Microsoft®
.NET

Motivation

- Multicores are **ubiquitous**
 - Parallel software skills **are not**
 - Parallel software is **neither**
 - **The free lunch is over** – clock frequency stagnation¹⁾
- ➔ Do we all have to become parallel experts now?
- Refactoring support for existing software needed.²⁾
- ➔ Automation?

1) David Paterson, Herb Sutter (2006, 2009)

2) Hans Vandierendonck: *Averting the Next Software Crisis* (2011)

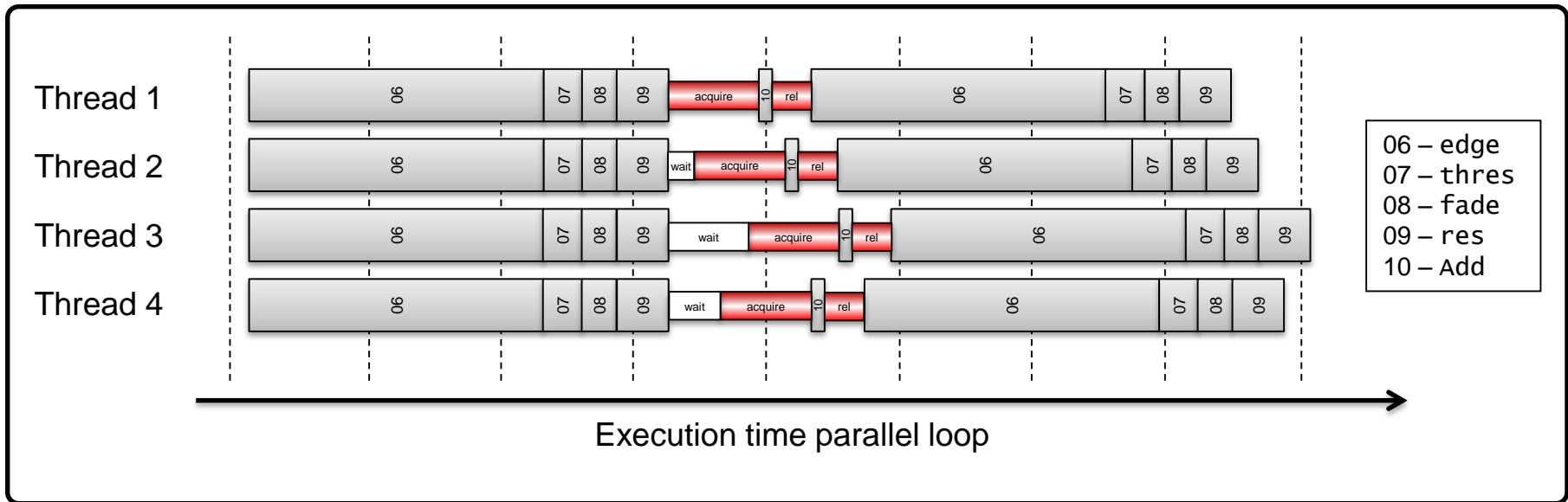
Motivation

```
01  AviStream Process(AviStream aviIn)
02  {
03  AviStream aviOut = new AviStream();
04  foreach(Image i in aviIn.Images)           1.440x
05  {
06  Image edge = edgeFilter.Apply(i);          68%
07  Image thres = thresholdFilter.Apply(i);    9%
08  Image fade = fadingFilter.Apply(i);        8%
09  Image res = addFilter.Apply(edge, thres, fade); 12%
10  aviOut.Images.Add(res);                   3%
11  }
12  return aviOut;
13  }
```

➔ Gold standard: Parallel loop?

AForge.NET – Library for Parallel Programming, <http://www.aforgenet.com>, 2013

Parallel loop



- Threads need to **wait very long** (aquisition/release of **common used lock**)
- Correct sequence **not guaranteed** without additional logic
- ➔ Can architecture patterns be used and derived automatically?

Architecture pattern: Pipeline

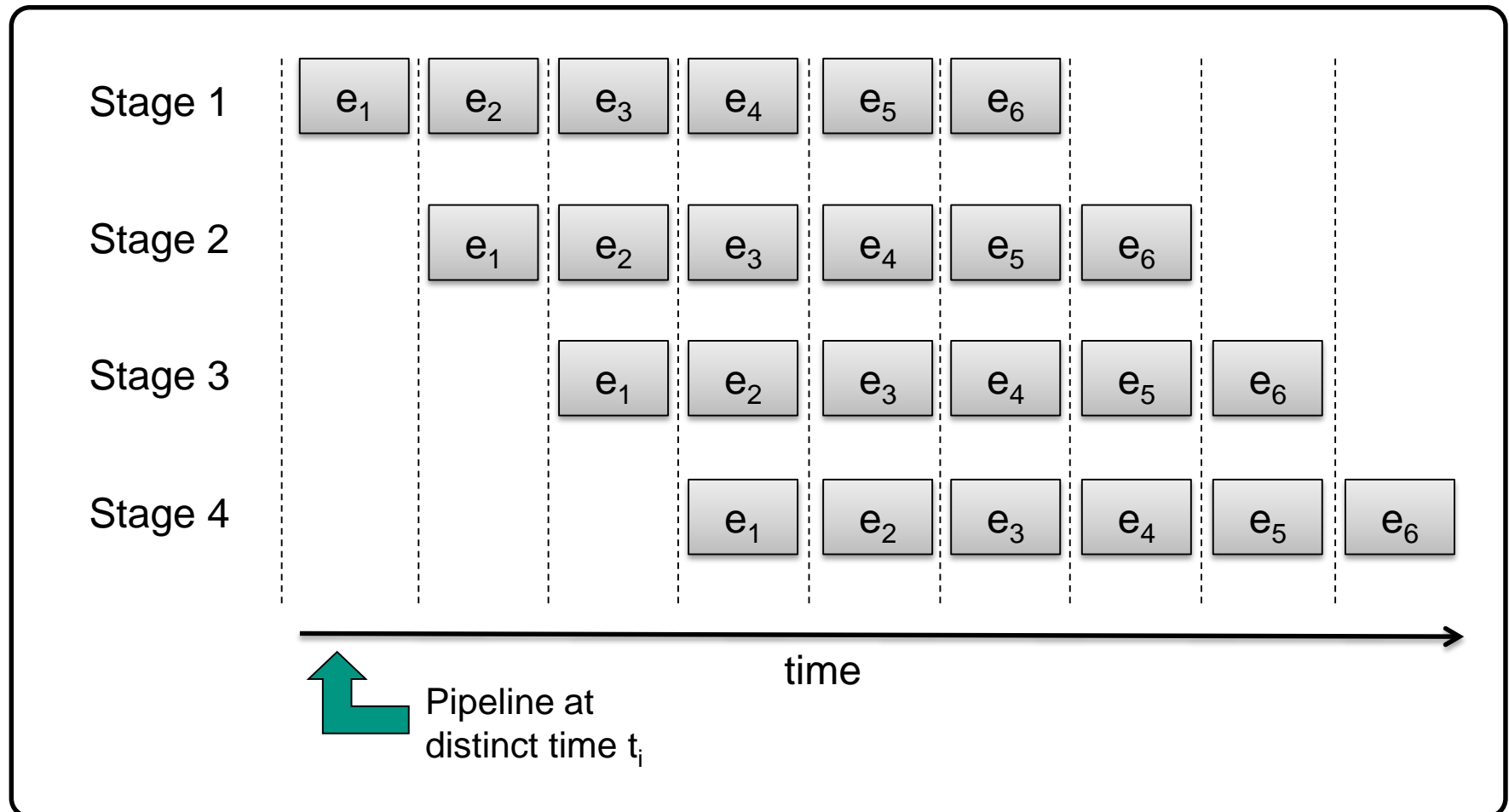


- Divide tasks into **different stages** that can be executed **consecutively**
- Dependencies between stages may exist (i.e. output of stage s_i is the input for the following stage s_{i+1})
 - ➔ Partly sequential execution, but dependencies **within procedures** and **across iterations** are preserved
- Data passes stages in a sequence
 - Might be cached between stages

Timothy G. Mattson, Beverly A. Sanders, Berna K. Massingill – *Patterns for Parallel Programming*, 2004

Architecture pattern: Pipeline

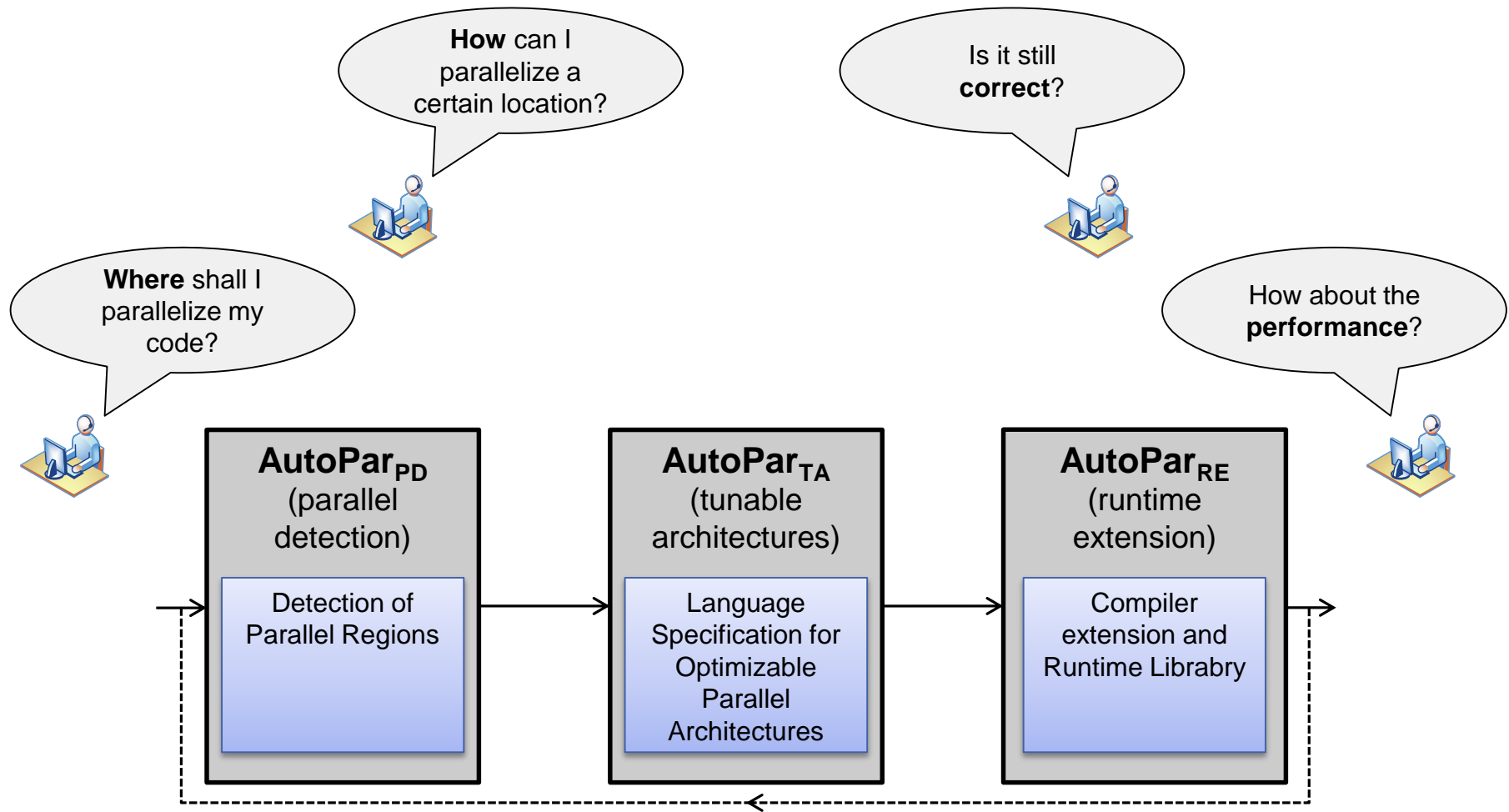
- Illustration for data elements e_1 to e_6



Observations

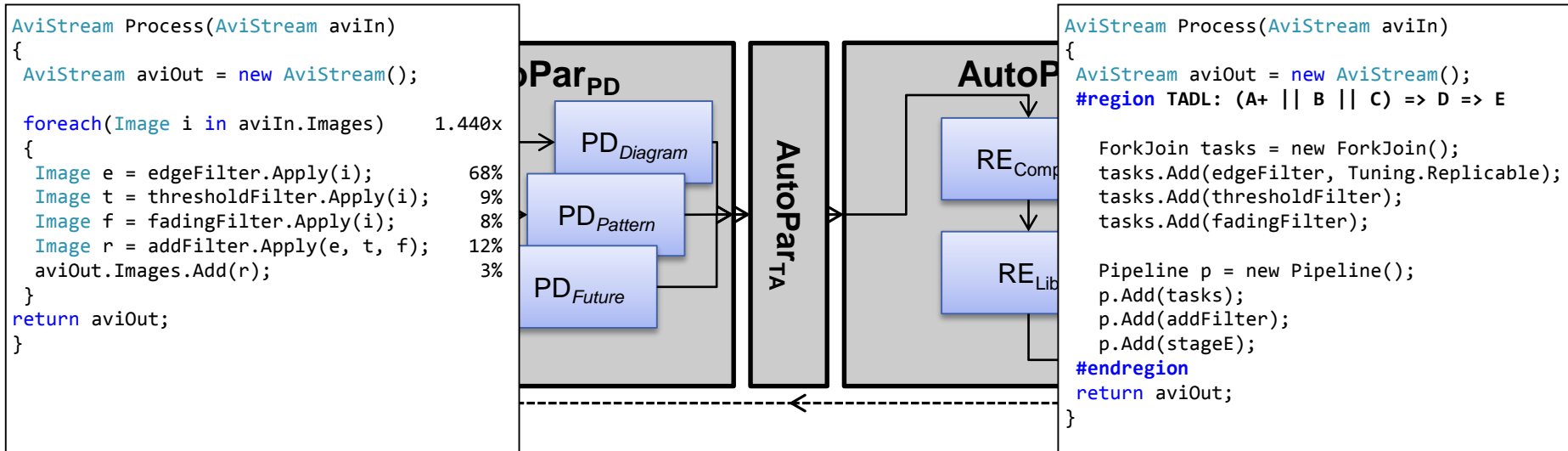
- Recurring **patterns exist**: Architecture / design / code access patterns...
 - ➡ Pattern-based approach
- Code areas **consume** different amounts of **runtime**
- Separation of concerns used in **object-orientation**
- Existing software builds on object-orientation
 - ➡ Split up control flow
- Modern object-oriented environments heavily use **references**
 - ➡ Combine **static** and **dynamic** analyses
- **Parameters** exist that have **influence** on the runtime behaviour
 - ➡ **Derive tuning parameters** from sequential runtime behaviour
- Race detectors exist but **not handy** for real-world applications
 - ➡ Unit tests as **small fractions** of a whole program

Pattern-based refactoring concept: AutoPar



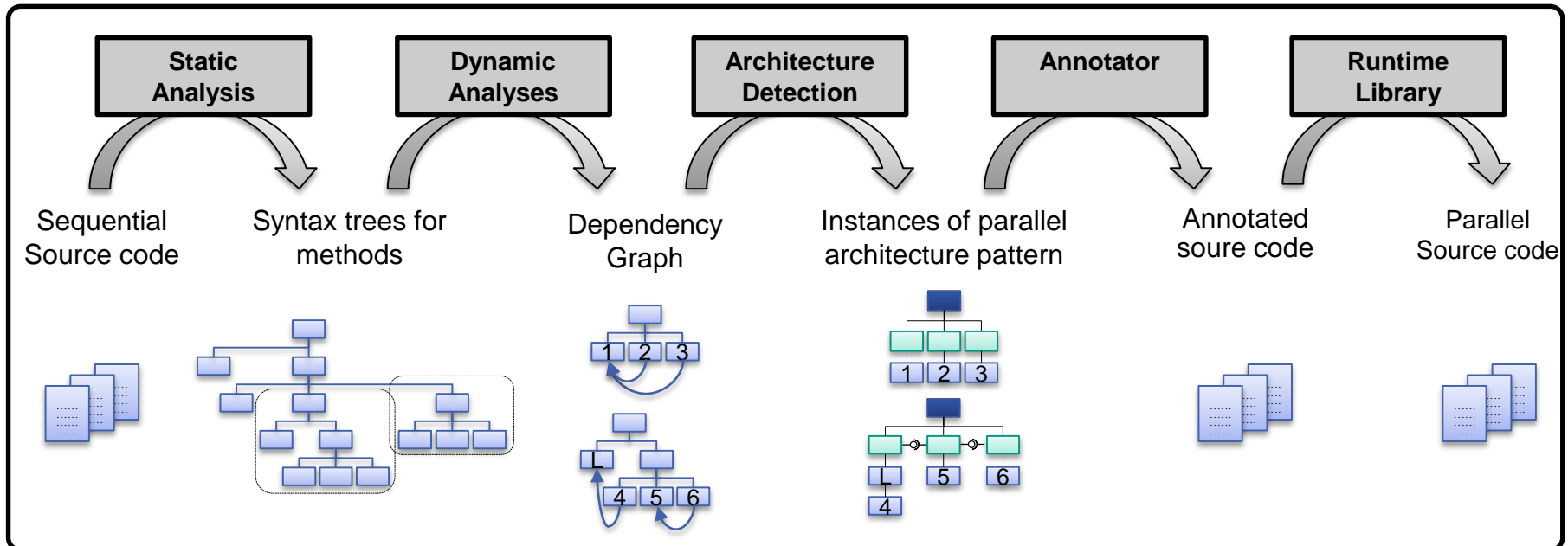
Korbinian Molitorisz, Jochen Schimmel, Frank Otto – *Automatic Parallelization using AutoFutures*, MSEPT 2012
 Jochen Schimmel, Korbinian Molitorisz, Ali Jannesari, Walter F. Tichy – *Automatic Generation of Parallel Unit Tests*, AST 2013

Pattern-based refactoring concept: AutoPar



- Analysis pattern: *Single Static Multiple Dynamic*
- Detection modules operate on *extended AST*
- Explicit *architecture language* with tuning information
- Runtime library with stencils for patterns
- Automatic unit test *generation*
- Interface for *auto tuners*

Pattern-based refactoring concept: AutoPar



- 5 separate steps to **parallelize** and **test correctness** in an automated process on the base of **architecture patterns**

Pattern-based refactoring concept: AutoPar

■ Code annotation

- Architecture description language with defined operators (for architecture description) and operands (for the architecture compartments)

■ Runtime library

- Input: Architecture description and architecture compartments
- Output: Instances of the runtime library, tuning file and unit tests

```

01 AviStream Process(AviStream aviIn)
02 {
03     AviStream aviOut = new AviStream();
04     #region TADL: (A+ || B || C) => D => E
05     foreach(Image i in aviIn.Images)
06     {
07         #region A: Image e = edgeFilter.Apply(i);           #endregion
08         #region B: Image t = thresholdFilter.Apply(i);      #endregion
09         #region C: Image f = fadingFilter.Apply(i);         #endregion
10         #region D: Image r = addFilter.Apply(e, t, f);      #endregion
11         #region E: aviOut.Images.Add(r);                   #endregion
12     }
13     #endregion
14     return aviOut;
15 }
  
```



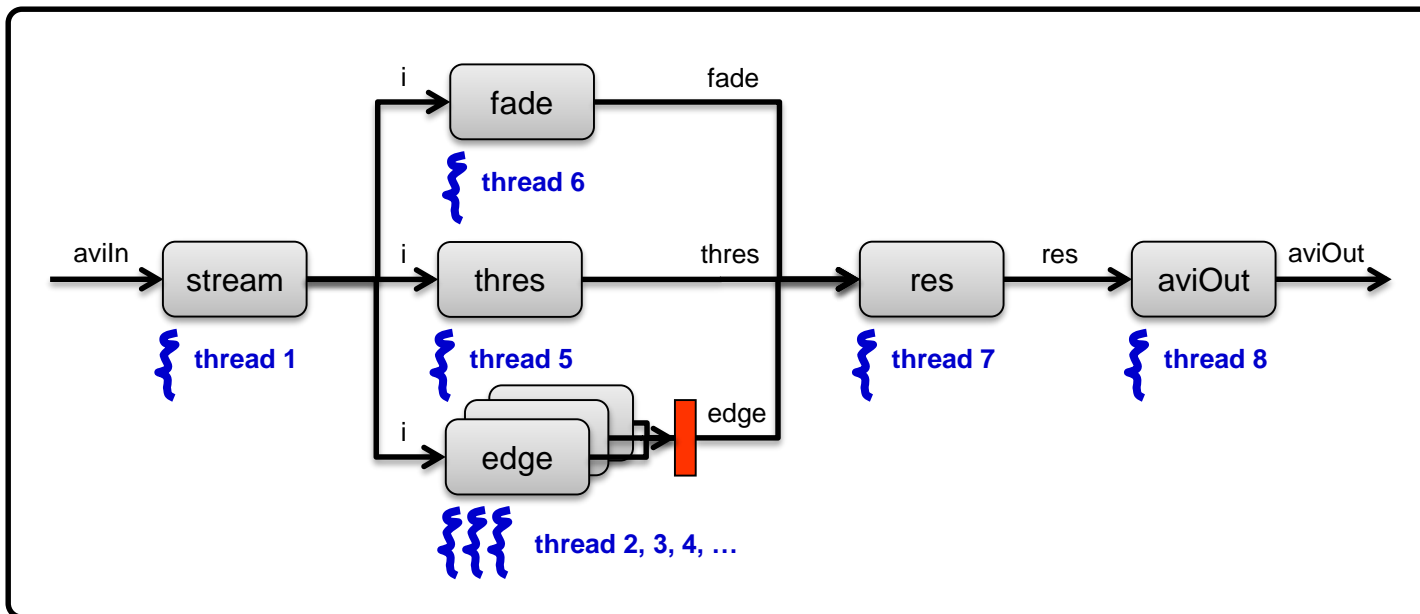
```

01 AviStream Process(AviStream aviIn)
02 {
03     Item p1 = new Item (edgeFilter.Apply());
04     Item p2 = new Item (thresholdFilter.Apply());
04     Item p3 = new Item (fadingFilter.Apply());
05     Item p4 = new Item (addFilter.Apply());
06     Item p5 = new Item (aviOut.Images.Add());
07     MasterWorker mw = new MasterWorker (p1, p2, p3);
08     mw.Item(p1).replicable = true;
10     Pipeline p = new Pipeline (mw, p4, p5);
11     p.Input = aviIn.Images;
12     p.Run();
13     return p.Output;
14 }
  
```

Back to the example...

■ Results:

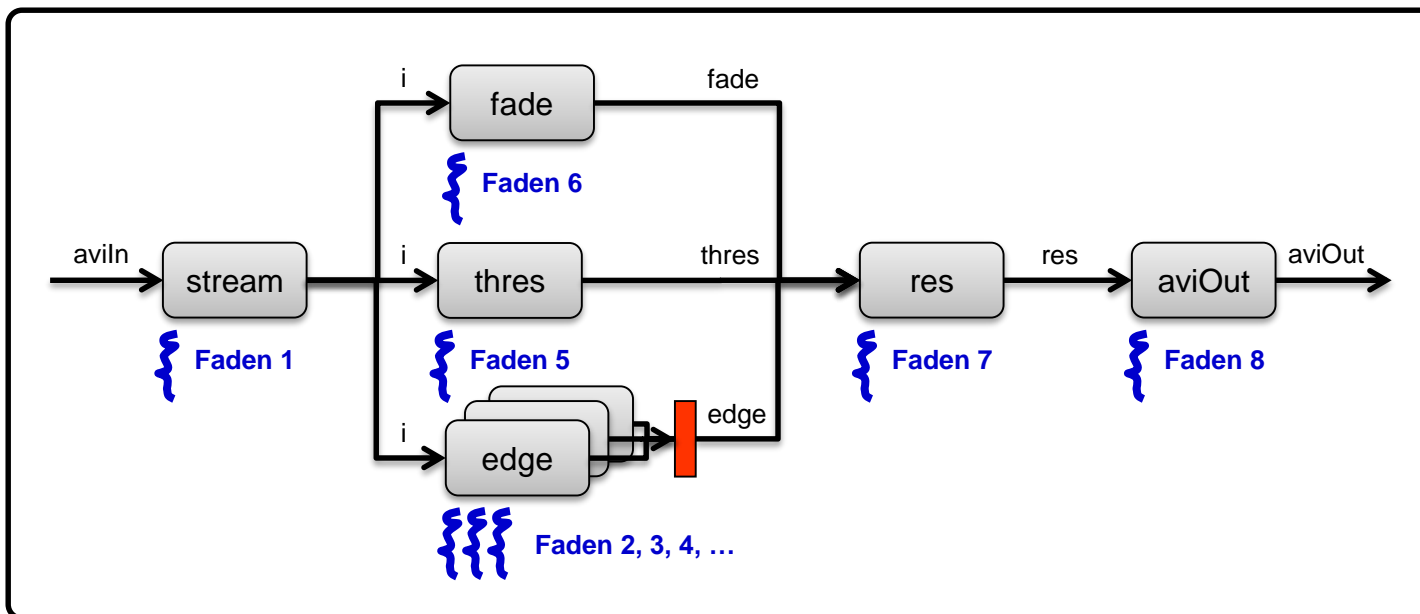
- Speedup on an 8-core machine: 3,12
- Manual implementation of this architecture pattern: 6,2



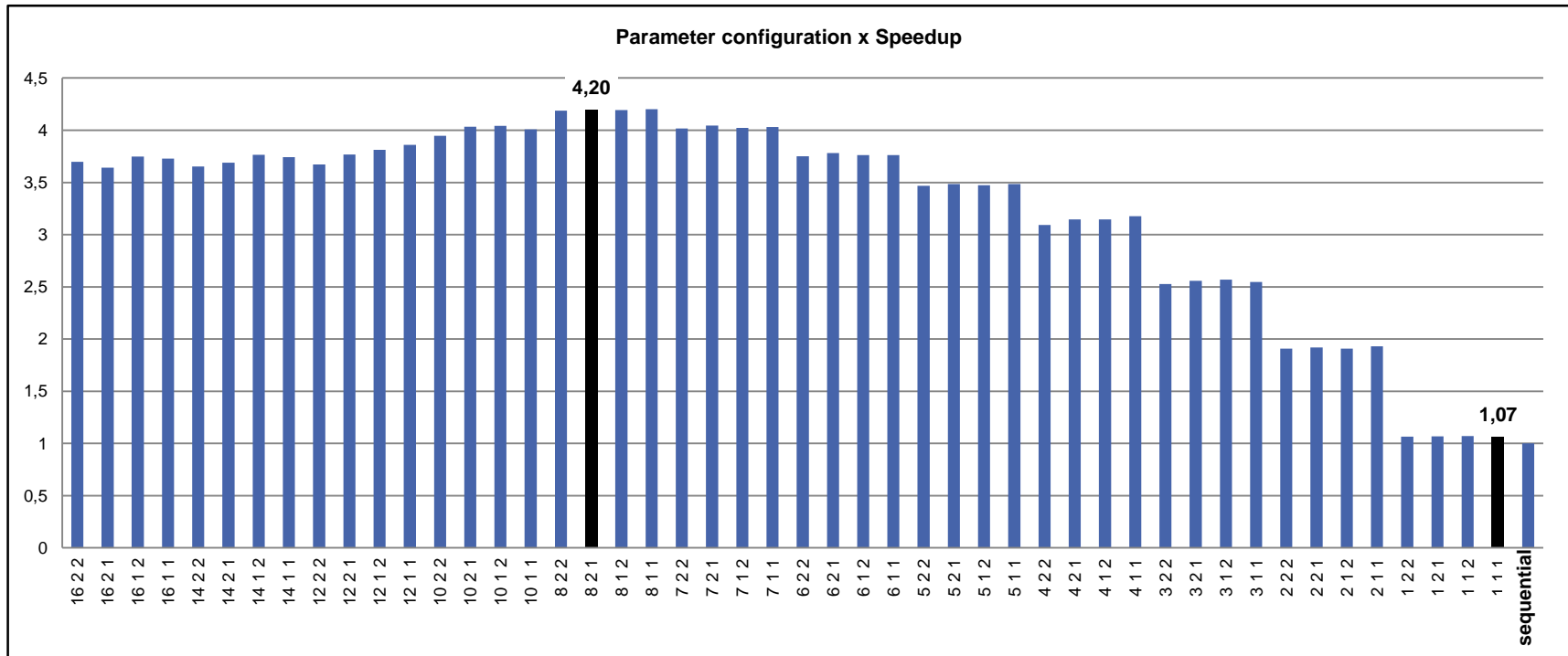
Back to the example...

Results:

- Speedup on an 8-core machine: 5,3
- Manual implementation of this architecture pattern: 6,2



Pattern-based refactoring concept: AutoPar



Evaluation

- 6 real-world projects
- 27.000 LOC
- Average search space reduction: 95%
- Average precision: 66%

Project	#HotSpots	#Architectures identified	Reduction	Correct & faster	Correct & -faster	-Correct	Precision
MergeSort	19	1	95%	1	0	0	100 %
RayTracer	63	6	90%	1	5	0	17 %
DesktopSearch	47	2	96%	1	1	0	50 %
CompGeo	169	1	98%	1	0	0	100 %
VideoProcessing	16	1	94%	1	0	0	100 %
PowerCollections	3.641	15	97%	4	9	2	27 %
	$\Sigma = 3.955$	$\Sigma = 26$	$\bar{\emptyset} = 95\%$	$\Sigma = 9$	$\Sigma = 15$	$\Sigma = 2$	$\bar{\emptyset} = 66\%$

Thank you for your attention.
Any questions?

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